



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Daniel Citron

Docket No.: IL920030047US1

Serial No.: 10/721,316

Group Art Unit: 2186

Filed: November 25, 2003

Examiner:

For: Reducing bus width by data compaction

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

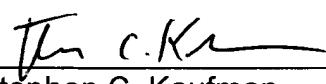
INFORMATION DISCLOSURE STATEMENT **TRANSMITTAL LETTER**

Sir:

Please find enclosed a listing (PTO Form 1449) of references which may be relevant to the above-referenced application. This listing of references is not a representation that a search has been made, that better references than listed do not exist, or that other references are not applicable. Moreover, this listing does not constitute any admission by applicants or applicants' attorney that the information provided herein is necessarily prior art to applicants' invention. **Please charge any fee necessary to enter this paper to deposit account 09-0468.**

Dated: March 16, 2005

Respectfully submitted,



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CERTIFICATE OF MAILING UNDER 37 CFR 1.8(a)

I hereby certify that the attached correspondence comprising:

1. Information Disclosure Statement Transmittal Letter;
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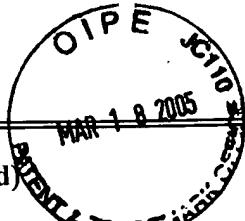
on March 16, 2005.

Jeanne M. Jordan

(Type or print name of person mailing paper)

Jeanne M. Jordan

(Signature of person mailing paper)



FORM PTO-1449 (Modified)

**LIST OF PATENTS AND PUBLICATIONS
FOR APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

ATTY. DOCKET NO.

IL920030047US1

SERIAL NO.:

10/721,316

APPLICANT:

Daniel Citron

FILING DATE:

November 25, 2003

GROUP: 2186

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AG							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

AH	Citron, Daniel et al., "Creating a Wider Bus Using Caching Techniques", Proceedings of the First IEEE Symposium on High-Performance Computer Architecture, January 1995, pages 90-99.
AI	Yang, Jun et al., "Frequent Value Compression in Data Caches", Proceedings of the 33rd International Symposium on Microarchitecture, December 2000, pages 258-275.
AJ	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.